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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/528,751 Confirmation No. : 4687
First Named Inventor : Markus ABERLE
Filed : March 20, 2006
TC/A.U. : 2173
Examiner : Not Yet Assigned

Docket No. : 102063.56895US
Customer No. : 23911

Title : Circuit Configuration and Method for Identifying Error
Situations in Interconnected Systems

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

On the attached copy of the Official Filing Receipt, it was noted that the title was incorrect. Please make a change to reflect the correct title which is -- Circuit Configuration and Method for Identifying error Situations in Interconnected Systems -- instead of "Circuit arrangement, in addition to method for identifying interruptions and short-circuits in coupled systems," as shown on the receipt.

Kindly return the "Corrected" Official Filing Receipt to the undersigned attorney of record.

Please charge any additional fee or credit any overpayment to the Deposit Account of Crowell & Moring, L.L.P., Account No. 05-1323, Docket No.: 102063.56895US.

Respectfully submitted,

October 4, 2006



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/528,751	03/20/2006	2173	1030	130309.507USPC	1	17	2

CONFIRMATION NO. 4687

500
SEED INTELLECTUAL PROPERTY LAW GROUP PLLC
701 FIFTH AVE
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SEATTLE, WA 98104-7092

FILING RECEIPT



OC000000018903615

Transferred:
9/2/05

Date Mailed: 06/01/2006

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Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 500.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP03/10641 09/24/2003

Foreign Applications

GERMANY 102 44 534.6 09/25/2002

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

Title

Configuration and

Error.. Situations in

Circuit arrangement, in addition to method for identifying interruptions and short-circuits in
coupled systems

Interconnected

Preliminary Class

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